L Number	Hits	Search Text	DB	Time stamp
1	137	(prom with write) and (cd or cdrom or compact adj disk)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:07
2	10775712	@ad<20000608	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:08
3	1142	firmware with updat$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:20
4	1	((prom with write) and (cd or cdrom or compact adj disk)) and (firmware with updat$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:09
5	642	@ad<20000608 and (firmware with updat$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:10
6	0	((prom with write) and (cd or cdrom or compact adj disk)) and (@ad<20000608 and (firmware with updat$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:10
7	518	connector same processor same (flash or prom or eprom or eeprom)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:21
8	18	(@ad<20000608 and (firmware with updat$3)) and (connector same processor same (flash or prom or eprom or eeprom))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:11
9	198	firmware.ab. with updat$3	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:20
10	130	@ad<20000608 and (firmware.ab. with updat$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:20
11	0	(connector same processor same (flash or prom or eprom or eeprom)) and (@ad<20000608 and (firmware.ab. with updat$3))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:21
12	0	(connector same processor same (flash or prom or eprom or eeprom)) and (firmware.ab. with updat$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:21
13	0	((prom with write) and (cd or cdrom or compact adj disk)) and (firmware.ab. with updat$3)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:21
14	17907	processor same (flash or prom or eprom or eeprom)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:22
15	25	(firmware.ab. with updat$3) and (processor same (flash or prom or eprom or eeprom))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:29
16	1		USPAT	2003/07/18 10:24
17	0	(selector with upgrade) same firmware	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:30
18	0	selector same upgrade same firmware	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:30

20	0	(firmware.ab. with updat$3) and (switch same upgrade same firmware)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:31
19	20	switch same upgrade same firmware	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:33
21	2	firmware adj write adj operation	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:34
22	93	firmware with (write adj operation)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:34
23	91	(firmware with (write adj operation)) not (firmware adj write adj operation)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:34
24	70	((firmware with (write adj operation)) not (firmware adj write adj operation)) and @ad<20000608	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:40
25	44491	program$5 with (flash or prom or eprom or eeprom)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:42
26	306	(connector same processor same (flash or prom or eprom or eeprom)) and (program$5 with (flash or prom or eprom or eeprom))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:42
27	0	(firmware.ab. with updat$3) and ((connector same processor same (flash or prom or eprom or eeprom)) and (program$5 with (flash or prom or eprom or eeprom)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:42
28	51	(firmware.ab. with updat$3) and (program$5 with (flash or prom or eprom or eeprom))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:42
29	34	@ad<20000608 and ((firmware.ab. with updat$3) and (program$5 with (flash or prom or eprom or eeprom)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:48
30	11192	update with signal	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:49
31	74	(firmware with updat$3) and (update with signal)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:49
32	17	(firmware.ab. with updat$3) and ((firmware with updat$3) and (update with signal))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:49
33	12	@ad<20000608 and ((firmware.ab. with updat$3) and ((firmware with updat$3) and (update with signal)))	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:54
34	147	711/102.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:55
35	849	711/103.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:59
36	2871	365/189.01.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:59

37	203	365/189.03.ccls.	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2003/07/18 10:59

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1 A high-level abstraction of shared accesses

Peter J. Keleher

February 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 1

Full text available: pdf(183.57 KB) Additional Information: full citation, abstract, references, index terms, review

We describe the design and use of the tape mechanism, a new high-level abstraction of accesses to shared data for software DSMs. Tapes consolidate and generalize a number of recent protocol optimizations, including update-based locks and recorded-replay barriers. Tapes are usually created by "recording" shared accesses. The resulting recordings can be used to anticipate future accesses by tailoring data movement to application semantics. Tapes-based mechanisms a ...

Keywords: DSM, programming libraries, shared memory, update protocols

2 Dynamic software updating

Michael Hicks, Jonathan T. Moore, Scott Nettles

May 2001 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2001 conference on Programming language design and implementation**, Volume 36 Issue 5

Full text available: pdf(1.44 MB) Additional Information: full citation, abstract, citings, index terms

Many important applications must run continuously and without interruption, yet must be changed to fix bugs or upgrade functionality. No prior general-purpose methodology for dynamic updating achieves a practical balance between flexibility, robustness, low overhead, and ease of use.

We present a new approach for C-like languages that provides type-safe dynamic updating of native code in an extremely flexible manner (code, data, and types may be updated, at programmer-determined times ...

3 The Roma personal metadata service

Edward Swierk, Emre Kiciman, Nathan C. Williams, Takashi Fukushima, Hideki Yoshida, Vince Laviano, Mary Baker

October 2002 **Mobile Networks and Applications**, Volume 7 Issue 5

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management problem of ensuring that the most up-to-date version of any document in a user's personal file space is available to him on the storage facility he is currently using. We introduce the Roma personal metadata service t ...

Keywords: data synchronization, distributed data storage, distributed databases, metadata, mobile computing, personal systems

4 Pen computing: a technology overview and a vision



André Meyer
July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3

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This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions.Starting with a short historic ...

5 Electronic document addressing: dealing with change



Helen Ashman
September 2000 **ACM Computing Surveys (CSUR)**, Volume 32 Issue 3

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The management of electronic document collections is fundamentally different from the management of paper documents. The ephemeral nature of some electronic documents means that the document address (i.e., reference details of the document) can become incorrect some time after coming into use, resulting in references, such as index entries and hypertext links, failing to correctly address the document they describe. A classic case of invalidated references is on the World Wide Web—lin ...

Keywords: 404, link, link integrity

6 Soft updates: a solution to the metadata update problem in file systems



Gregory R. Ganger, Marshall Kirk McKusick, Craig A. N. Soules, Yale N. Patt
May 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 2

Full text available: pdf(147.90 KB) Additional Information: full citation, abstract, references, citings, index terms

Metadata updates, such as file creation and block allocation, have consistently been identified as a source of performance, integrity, security, and availability problems for file systems. Soft updates is an implementation technique for low-cost sequencing of fine-grained updates to write-back cache blocks. Using soft updates to track and enforce metadata update dependencies, a file system can safely use delayed writes for almost all file operations. This article describes soft ...

7 Kernel Korner



Joseph Pranevich
December 1998 **Linux Journal**

Full text available: html(22.19 KB) Additional Information: full citation, abstract, index terms

The Wonderful World of Linux 2.2: Mr. Pranevich gives us a look at the changes and improvements coming out in the new kernel

8 Embedding Linux in a Commercial Product: A look at embedded systems and what it takes to build one



Joel R. Williams
January 1999 **Linux Journal**

Full text available: html(32.45 KB) Additional Information: full citation, index terms

9 Book review: Review of Understanding accessibility - a guide to achieving compliance on websites and intranets by Robert B. Yonaitis. HiSoftware, 2002; Building accessible websites by Joe Clark. New Riders, 2002; Constructing accessible websites by Jim Thatcher, Cynthia Waddell, Shawn Henry, Sarah Swierenga, Mark Urban, Michael Burks, Bob Regan, and Paul Bohman. Glasshaus, 2002; Maximum accessibility by John M. Slatin and Sharron Rush. Addison Wesley Professional, 2002.
Charles McCathieNevile
May 2003 **ACM SIGCHI Bulletin - a supplement to *interactions***, Issue 2

Full text available: pdf(178.41 KB) Additional Information: full citation

10 Illustrative risks to the public in the use of computer systems and related technology
Peter G. Neumann
January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1

Full text available: pdf(2.54 MB) Additional Information: full citation

11 HyperNews: a MEDIA application for the commercialization of an electronic newspaper
Jean-Henry Morin, Dimitri Konstantas
February 1998 **Proceedings of the 1998 ACM symposium on Applied Computing**

Full text available: pdf(2.74 MB) Additional Information: full citation, references, index terms

Keywords: agents, copyright protection, electronic publishing

12 Linux and the New Internet Computer
Bill Ball
February 2001 **Linux Journal**

Full text available: html(13.85 KB) Additional Information: full citation, index terms

13 Live documents with contextual, data-driven information components
Anke Weber, Holger M. Kienle, Hausi A. Müller
October 2002 **Proceedings of the 20th annual international conference on Computer documentation**

Full text available: pdf(627.10 KB) Additional Information: full citation, abstract, references, index terms

We introduce the notion of a live document and we describe our concept of live documents with contextual, data driven information components. The dynamic and interactive features of live documents provide a consistent data source for multimedia presentations targeted to various audiences and multiple platforms. Therefore, they contribute to the solution of key challenges in single sourcing and repurposing. We motivate the use of live documents with sample scenarios from the field of systems docu ...

Keywords: Microsoft Office, live documents, repurposing, reverse engineering, scalable vector graphics, single sourcing, software engineering, systems documentation

14 On representation of a highlight on the Web: the amber room as a cultural phenomenon in progress

Tatyana G. Bogomazova, Cyrill A. Malevanov

April 2001 **Proceedings of the tenth international conference on World Wide Web**

Full text available: pdf(417.75 KB) Additional Information: full citation, references, index terms

15 DBMS implementation experience: FLASH: a language-independent, portable file access system

James E. Allchin, Arthur M. Keller, Gio Wiederhold

May 1980 **Proceedings of the 1980 ACM SIGMOD international conference on Management of data**

Full text available: pdf(709.40 KB) Additional Information: full citation, abstract, references

A file access system, flash, for use in building database systems is described. It supports access from several languages, including pascal, fortran, and interlisp. Flash provides record level access to a file with multiple indexes using symbolic keys. It is portable and written in Pascal with support routines in dec System 20 macro. The file access system is designed to run on computers of various sizes and capabilities, including micros. Co ...

Keywords: ISAM, Indexed sequential, VSAM, concurrent update, databases, files, microprocessors, multiple indexes, multiple language, portable

16 eNVy: a non-volatile, main memory storage system

Michael Wu, Willy Zwaenepoel

November 1994 **Proceedings of the sixth international conference on Architectural support for programming languages and operating systems**, Volume 29 , 28 Issue 11 , 5

Full text available: pdf(1.32 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes the architecture of eNVy, a large non-volatile main memory storage system built primarily with Flash memory. eNVy presents its storage space as a linear, memory mapped array rather than as an emulated disk in order to provide an efficient and easy to use software interface.Flash memories provide persistent storage with solid-state memory access times at a lower cost than other solid-state technologies. However, they have a number of drawbacks. Flash chips are ...

17 Product-line architectures in industry: a case study

Jan Bosch

May 1999 **Proceedings of the 21st international conference on Software engineering**

Full text available: pdf(1.70 MB) Additional Information: full citation, references, citings, index terms

Keywords: case study, experiences, product-line architectures

18 The trials and tribulations of installing LinuxPPC 2000 Q4

Paul Barry

August 2001 **Linux Journal**, Volume 2001 Issue 88

Full text available: html(20.70 KB) Additional Information: full citation, abstract, index terms

How easy is it to use Linux on a Mac?

19 Review: SuSE 7.2 professional

Don Marti
September 2001 **Linux Journal**, Volume 2001 Issue 89

Full text available: html(7.95 KB) Additional Information: full citation, index terms

20 New Products
July 2000 **Linux Journal**

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1 Pen computing: a technology overview and a vision

André Meyer

July 1995 **ACM SIGCHI Bulletin**, Volume 27 Issue 3

Full text available: pdf(5.14 MB) Additional Information: full citation, abstract, citings, index terms

This work gives an overview of a new technology that is attracting growing interest in public as well as in the computer industry itself. The visible difference from other technologies is in the use of a pen or pencil as the primary means of interaction between a user and a machine, picking up the familiar pen and paper interface metaphor. From this follows a set of consequences that will be analyzed and put into context with other emerging technologies and visions.Starting with a short historic ...

2 Embedding Linux in a Commercial Product: A look at embedded systems and what it takes to build one

Joel R. Williams

January 1999 **Linux Journal**

Full text available: html(32.45 KB) Additional Information: full citation, index terms

3 Caches and Memory Systems: Patchable instruction ROM architecture

Timothy Sherwood, Brad Calder

November 2001 **Proceedings of the international conference on Compilers, architecture, and synthesis for embedded systems**

Full text available: pdf(299.03 KB) Additional Information: full citation, abstract, references

Increased systems level integration has meant the movement of many traditionally off chip components onto a single chip including a processor, instruction storage, data path, and local memory. The design of these systems is driven by two conflicting goals, the need for reduced area and the need for rapid development times. The two current design options for instruction storage, ROM and Flash, are each highly optimized to one of these two goals but provide little compromise between them. ROM is u ...

4 A high-level abstraction of shared accesses

Peter J. Keleher

February 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 1

Full text available: pdf(183.57 KB) Additional Information: full citation, abstract, references, index terms, review

We describe the design and use of the tape mechanism, a new high-level abstraction of

accesses to shared data for software DSMs. Tapes consolidate and generalize a number of recent protocol optimizations, including update-based locks and recorded-replay barriers. Tapes are usually created by "recording" shared accesses. The resulting recordings can be used to anticipate future accesses by tailoring data movement to application semantics. Tapes-based mechanisms a ...

Keywords: DSM, programming libraries, shared memory, update protocols

5 The Roma personal metadata service

Edward Swierk, Emre Kiciman, Nathan C. Williams, Takashi Fukushima, Hideki Yoshida, Vince Laviano, Mary Baker

October 2002 **Mobile Networks and Applications**, Volume 7 Issue 5

Full text available: pdf(221.38 KB) Additional Information: full citation, abstract, references, index terms

People now have available to them a diversity of digital storage facilities, including laptops, cell phone address books, handheld devices, desktop computers and web-based storage services. Unfortunately, as the number of personal data repositories increases, so does the management problem of ensuring that the most up-to-date version of any document in a user's personal file space is available to him on the storage facility he is currently using. We introduce the Roma personal metadata service t ...

Keywords: data synchronization, distributed data storage, distributed databases, metadata, mobile computing, personal systems

6 Live documents with contextual, data-driven information components

Anke Weber, Holger M. Kienle, Hausi A. Müller

October 2002 **Proceedings of the 20th annual international conference on Computer documentation**

Full text available: pdf(627.10 KB) Additional Information: full citation, abstract, references, index terms

We introduce the notion of a live document and we describe our concept of live documents with contextual, data driven information components. The dynamic and interactive features of live documents provide a consistent data source for multimedia presentations targeted to various audiences and multiple platforms. Therefore, they contribute to the solution of key challenges in single sourcing and repurposing. We motivate the use of live documents with sample scenarios from the field of systems docu ...

Keywords: Microsoft Office, live documents, repurposing, reverse engineering, scalable vector graphics, single sourcing, software engineering, systems documentation

7 Illustrative risks to the public in the use of computer systems and related technology

Peter G. Neumann

January 1996 **ACM SIGSOFT Software Engineering Notes**, Volume 21 Issue 1

Full text available: pdf(2.54 MB) Additional Information: full citation

8 Linux and the New Internet Computer

Bill Ball

February 2001 **Linux Journal**

Full text available: html(13.85 KB) Additional Information: full citation, index terms

9 Piranha: a scalable architecture based on single-chip multiprocessing

Luiz André Barroso, Kourosh Gharachorloo, Robert McNamara, Andreas Nowatzyk, Shaz

Qadeer, Barton Sano, Scott Smith, Robert Stets, Ben Verghese
May 2000 **ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual international symposium on Computer architecture**, Volume 28 Issue 2

Full text available: pdf(191.10 KB) Additional Information: full citation, abstract, references, citings, index terms

The microprocessor industry is currently struggling with higher development costs and longer design times that arise from exceedingly complex processors that are pushing the limits of instruction-level parallelism. Meanwhile, such designs are especially ill suited for important commercial applications, such as on-line transaction processing (OLTP), which suffer from large memory stall times and exhibit little instruction-level parallelism. Given that commercial applications constitute by fa ...

10 eNVy: a non-volatile, main memory storage system

Michael Wu, Willy Zwaenepoel
November 1994 **Proceedings of the sixth international conference on Architectural support for programming languages and operating systems**, Volume 29 , 28 Issue 11 , 5

Full text available: pdf(1.32 MB) Additional Information: full citation, abstract, references, citings, index terms

This paper describes the architecture of eNVy, a large non-volatile main memory storage system built primarily with Flash memory. eNVy presents its storage space as a linear, memory mapped array rather than as an emulated disk in order to provide an efficient and easy to use software interface.Flash memories provide persistent storage with solid-state memory access times at a lower cost than other solid-state technologies. However, they have a number of drawbacks. Flash chips are ...

11 Two methods for display of high contrast images

Jack Tumblin, Jessica K. Hodgins, Brian K. Guenter
January 1999 **ACM Transactions on Graphics (TOG)**, Volume 18 Issue 1

Full text available: pdf(10.28 MB) Additional Information: full citation, abstract, references, citings, index terms, review

High contrast images are common in night scenes and other scenes that include dark shadows and bright light sources. These scenes are difficult to display because their contrasts greatly exceed the range of most display devices for images. As a result, the image constrasts are compressed or truncated, obscuring subtle textures and details. Humans view and understand high contrast scenes easily, "adapting" their visual response to avoid compression or truncation with no apparent ...

Keywords: adaptation, tone reproduction, visual appearance

12 We Talk to Everybody

Marjorie Richardson, Jason Schumaker, David Penn
June 2000 **Linux Journal**

Full text available: html(96.53 KB) Additional Information: full citation, abstract, index terms

A quick look at some of the people who helped make Linux possible.

13 Rover: a toolkit for mobile information access

A. D. Joseph, A. F. de Lespinasse, J. A. Tauber, D. K. Gifford, M. F. Kaashoek
December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5

Full text available: pdf(2.18 MB) Additional Information: full citation, references, citings, index terms

14 Ace: a language for parallel programming with customizable protocols

Mukund Raghavachari, Anne Rogers
August 1999 **ACM Transactions on Computer Systems (TOCS)**, Volume 17 Issue 3

Full text available: pdf(297.50 KB) Additional Information: full citation, abstract, references, index terms, review

Customizing the protocols that manage accesses to different data structures within an application can improve the performance of software shared-memory programs substantially. Existing systems for using customizable protocols are hard to use directly because the mechanisms they provide for manipulating protocols are low-level ones. This article is an in-depth study of the issues involved in providing language support for application-specific protocols. We describe the design and implementat ...

Keywords: parallel processing

15 WordPerfect Office 2000 Deluxe
Jon Valesh
August 2000 **Linux Journal**

Full text available: html(23.28 KB) Additional Information: full citation, index terms

16 Parity logging disk arrays
Daniel Stodolsky, Mark Holland, William V. Courtright, Garth A. Gibson
August 1994 **ACM Transactions on Computer Systems (TOCS)**, Volume 12 Issue 3

Full text available: pdf(1.98 MB) Additional Information: full citation, abstract, references, citings, index terms

Parity-encoded redundant disk arrays provide highly reliable, cost-effective secondary storage with high performance for reads and large writes. Their performance on small writes, however, is much worse than mirrored disks—the traditional, highly reliable, but expensive organization for secondary storage. Unfortunately, small writes are a substantial portion of the I/O workload of many important, demanding applications such as on-line transaction processing. This paper presents
Keywords: RAID, disk arrays

17 Coherent network interfaces for fine-grain communication
Shubhendu S. Mukherjee, Babak Falsafi, Mark D. Hill, David A. Wood
May 1996 **ACM SIGARCH Computer Architecture News , Proceedings of the 23rd annual international symposium on Computer architecture**, Volume 24 Issue 2

Full text available: pdf(1.72 MB) Additional Information: full citation, abstract, references, citings, index terms

Historically, processor accesses to memory-mapped device registers have been marked uncachable to insure their visibility to the device. The ubiquity of snooping cache coherence, however, makes it possible for processors and devices to interact with cachable, coherent memory operations. Using coherence can improve performance by facilitating burst transfers of whole cache blocks and reducing control overheads (e.g., for polling).This paper begins an exploration of network interfaces (NIs) that u ...

18 Group consistency model which separates the intra-group consistency maintenance from the inter-group consistency maintenance in large scale DSM systems
Qun Li, Hua Ji, Li Xie
April 1997 **ACM SIGOPS Operating Systems Review**, Volume 31 Issue 2

Full text available: pdf(824.47 KB) Additional Information: full citation, abstract, citings, index terms

According to the characteristics of large scale network computing systems, we proposed a group consistency model based on the concept of group to construct a DSM system. The novel model can use different inter-group and intra-group consistencies and lend itself to

flexible, easily-managable, and application-suitable DSM in large scale systems. A group consistency model, which applies entry consistency among groups and lazy release consistency in a group, together with its implementation policy i ...

Keywords: distributed shared memory, entry consistency model, memory coherence, release consistency model

19 Cooking with Linux: Launch, not lunch platforms!

Marcel Gagné

August 2001 **Linux Journal**, Volume 2001 Issue 88

Full text available: html(13.02 KB) Additional Information: full citation, index terms

20 Dynamic software updating

Michael Hicks, Jonathan T. Moore, Scott Nettles

May 2001 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2001 conference on Programming language design and implementation**, Volume 36 Issue 5

Full text available: pdf(1.44 MB) Additional Information: full citation, abstract, citings, index terms

Many important applications must run continuously and without interruption, yet must be changed to fix bugs or upgrade functionality. No prior general-purpose methodology for dynamic updating achieves a practical balance between flexibility, robustness, low overhead, and ease of use.

We present a new approach for C-like languages that provides type-safe dynamic updating of native code in an extremely flexible manner (code, data, and types may be updated, at programmer-determined times ...

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21 Distributed indexing: a scalable mechanism for distributed information retrieval

Peter B. Danzig, Jongsuk Ahn, John Noll, Katia Obraczka

September 1991 **Proceedings of the 14th annual international ACM SIGIR conference on Research and development in information retrieval**

Full text available: pdf(929.40 KB) Additional Information: full citation, references, citings, index terms

22 The SHRIMP performance monitor: design and applications

Margaret Martonosi, Douglas W. Clark, Malena Mesarina

January 1996 **Proceedings of the SIGMETRICS symposium on Parallel and distributed tools**

Full text available: pdf(1.01 MB) Additional Information: full citation, references, citings, index terms

23 AI update: AI update

D. R. Hobaugh

December 2001 **intelligence**, Volume 12 Issue 4

Full text available: pdf(356.69 KB) Additional Information: full citation, index terms

24 Performance of cache coherence in stackable filing

J. Heidemann, G. Popek

December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles**, Volume 29 Issue 5

Full text available: pdf(2.00 MB) Additional Information: full citation, references, index terms

25 Reactive synchronization algorithms for multiprocessors

Beng-Hong Lim, Anant Agarwal

November 1994 **Proceedings of the sixth international conference on Architectural support for programming languages and operating systems**, Volume 29 , 28 Issue 11 , 5

Full text available: pdf(1.27 MB) Additional Information: full citation, abstract, references, citings, index terms

Synchronization algorithms that are efficient across a wide range of applications and operating conditions are hard to design because their performance depends on

unpredictable run-time factors. The designer of a synchronization algorithm has a choice of protocols to use for implementing the synchronization operation. For example, candidate protocols for locks include test-and-set protocols and queueing protocols. Frequently, the best choice of protocols depends on the leve ...

26 Integration of message passing and shared memory in the Stanford FLASH multiprocessor

John Heinlein, Kourosh Gharachorloo, Scott Dresser, Anoop Gupta

November 1994 **Proceedings of the sixth international conference on Architectural support for programming languages and operating systems**, Volume 29 , 28 Issue 11 , 5

Full text available: pdf(1.80 MB) Additional Information: full citation, abstract, references, citings, index terms

The advantages of using message passing over shared memory for certain types of communication and synchronization have provided an incentive to integrate both models within a single architecture. A key goal of the FLASH (FLexible Architecture for SHared memory) project at Stanford is to achieve this integration while maintaining a simple and efficient design. This paper presents the hardware and software mechanisms in FLASH to support various message passing protocols. We achieve low overhe ...

27 Speculative synchronization: applying thread-level speculation to explicitly parallel applications

José F. Martínez, Josep Torrellas

October 2002 **Tenth international conference on architectural support for programming languages and operating systems on Proceedings of the 10th international conference on architectural support for programming languages and operating systems (ASPLOS-X)**, Volume 36 , 30 , 37 Issue 5 , 5 , 10

Full text available: pdf(1.49 MB) Additional Information: full citation, abstract, references

Barriers, locks, and flags are synchronizing operations widely used programmers and parallelizing compilers to produce race-free parallel programs. Often times, these operations are placed suboptimally, either because of conservative assumptions about the program, or merely for code simplicity.We propose *Speculative Synchronization,* which applies the philosophy behind Thread-Level Speculation (TLS) to explicitly parallel applications. Speculative threads execute past active barriers, busy ...

28 Integrating performance monitoring and communication in parallel computers

Margaret Martonosi, David Ofelt, Mark Heinrich

May 1996 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1996 ACM SIGMETRICS international conference on Measurement and modeling of computer systems**, Volume 24 Issue 1

Full text available: pdf(1.49 MB) Additional Information: full citation, abstract, references, citings, index terms

A large and increasing gap exists between processor and memory speeds in scalable cache-coherent multiprocessors. To cope with this situation, programmers and compiler writers must increasingly be aware of the memory hierarchy as they implement software. Tools to support memory performance tuning have, however, been hobbled by the fact that it is difficult to observe the caching behavior of a running program. Little hardware support exists specifically for observing caching behavior; furthermore ...

29 Improving static and dynamic registration in an optical see-through HMD

Ronald Azuma, Gary Bishop

July 1994 **Proceedings of the 21st annual conference on Computer graphics and interactive techniques**

Full text available: pdf(321.33 KB) ps(1.65 MB) Additional Information: full citation, abstract, references, citings, index terms

In Augmented Reality, see-through HMDs superimpose virtual 3D objects on the real world.

This technology has the potential to enhance a user's perception and interaction with the real world. However, many Augmented Reality applications will not be accepted until we can accurately register virtual objects with their real counterparts. In previous systems, such registration was achieved only from a limited range of viewpoints, when the user kept his head still. This paper offers improved regi ...

Keywords: augmented reality, calibration, registration

30 Storing text retrieval systems on CD-ROM: compression and encryption considerations

Shmuel T. Klein, Abraham Bookstein, Scott Deerwester

July 1989 **ACM Transactions on Information Systems (TOIS)**, Volume 7 Issue 3

Full text available: pdf(1.53 MB) Additional Information: full citation, abstract, references, citings, index terms, review

The emergence of the CD-ROM as a storage medium for full-text databases raises the question of the maximum size database that can be contained by this medium. As an example, the problem of storing the Trésor de la Langue Fran&ccidel;aise on a CD-ROM is examined in this paper. The text alone of this database is 700 megabytes long, more than a CD-ROM can hold. In addition, the dictionary and concordance needed to access these data must be stored. A further constraint is that some of th ...

31 Witan web and the software engineering of web-based applications

J. Howard Johnson, Stephen A. Mackay

November 2000 **Proceedings of the 2000 conference of the Centre for Advanced Studies on Collaborative research**

Full text available: pdf(653.32 KB) Additional Information: full citation, abstract, references, index terms

WitanWeb is a web-based application that supports the interactions among program committee members, authors, and reviewers for the refereeing process of conferences. Since development began in 1997, it has evolved a great deal to support the requirements of CASCON, WWW8, and ICSE 2001 as well as several other significant conferences. The development focus has emphasized usability related to the task as well as security, integrity, and availability to encourage the development of trust by each cl ...

32 Software versus hardware shared-memory implementation: a case study

A. L. Cox, S. Dwarkadas, P. Keleher, H. Lu, R. Rajamony, W. Zwaenepoel

April 1994 **ACM SIGARCH Computer Architecture News , Proceedings of the 21ST annual international symposium on Computer architecture**, Volume 22 Issue 2

Full text available: pdf(1.08 MB) Additional Information: full citation, abstract, references, citings, index terms

We compare the performance of software-supported shared memory on a general-purpose network to hardware-supported shared memory on a dedicated interconnect.Up to eight processors, our results are based on the execution of a set of application programs on a SGI 4D/480 multiprocessor and on TreadMarks, a distributed shared memory system that runs on a Fore ATM LAN of DECstation-5000/240s. Since the DECstation and the 4D/480 use the same processor, primary cache, and compiler, the shared-memory imp ...

33 Enhancing software reliability with speculative threads

Jeffrey Oplinger, Monica S. Lam

October 2002 **Tenth international conference on architectural support for programming languages and operating systems on Proceedings of the 10th international conference on architectural support for programming languages and operating systems (ASPLOS-X)**, Volume 37 , 36 , 30 Issue 10 , 5 , 5

Full text available: pdf(1.47 MB) Additional Information: full citation, abstract, references

This paper advocates the use of a monitor-and-recover programming paradigm to enhance the reliability of software, and proposes an architectural design that allows software and hardware to cooperate in making this paradigm more efficient and easier to program.We

propose that programmers write monitoring functions assuming simple sequential execution semantics. Our architecture speeds up the computation by executing the monitoring functions speculatively in parallel with the main computation. For ...

34 The state of the art in locally distributed Web-server systems

Valeria Cardellini, Emiliano Casalicchio, Michele Colajanni, Philip S. Yu

June 2002 **ACM Computing Surveys (CSUR)**, Volume 34 Issue 2

Full text available: pdf(1.41 MB) Additional Information: full citation, abstract, references, index terms

The overall increase in traffic on the World Wide Web is augmenting user-perceived response times from popular Web sites, especially in conjunction with special events. System platforms that do not replicate information content cannot provide the needed scalability to handle large traffic volumes and to match rapid and dramatic changes in the number of clients. The need to improve the performance of Web-based services has produced a variety of novel content delivery architectures. This article w ...

Keywords: Client/server, World Wide Web, cluster-based architectures, dispatching algorithms, distributed systems, load balancing, routing mechanisms

35 IS '97: model curriculum and guidelines for undergraduate degree programs in information systems

Gordon B. Davis, John T. Gorgone, J. Daniel Couger, David L. Feinstein, Herbert E. Longenecker

December 1997 **ACM SIGMIS Database , Guidelines for undergraduate degree programs on Model curriculum and guidelines for undergraduate degree programs in information systems**, Volume 28 Issue 1

Full text available: pdf(7.24 MB) Additional Information: full citation, citings

36 Electronic document addressing: dealing with change

Helen Ashman

September 2000 **ACM Computing Surveys (CSUR)**, Volume 32 Issue 3

Full text available: pdf(92.20 KB) Additional Information: full citation, abstract, references, citings, index terms, review

The management of electronic document collections is fundamentally different from the management of paper documents. The ephemeral nature of some electronic documents means that the document address (i.e., reference details of the document) can become incorrect some time after coming into use, resulting in references, such as index entries and hypertext links, failing to correctly address the document they describe. A classic case of invalidated references is on the World Wide Web—lin ...

Keywords: 404, link, link integrity

37 On the value locality of store instructions

Kevin M. Lepak, Mikko H. Lipasti

May 2000 **ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual international symposium on Computer architecture**, Volume 28 Issue 2

Full text available: pdf(149.50 KB) Additional Information: full citation, abstract, references, citings, index terms

Value locality, a recently discovered program attribute that describes the likelihood of the recurrence of previously-seen program values, has been studied enthusiastically in the recent published literature. Much of the energy has focused on refining the initial efforts at predicting load instruction outcomes, with the balance of the effort examining the value locality of either all register-writing instructions, or a focused subset of them. Surprisingly, there has been very little publish ...

38 Measuring experimental error in microprocessor simulation

Rajagopalan Desikan, Doug Burger, Stephen W. Keckler

May 2001 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 2001 symposium on Software reusability: putting software reuse in context**, Volume 26 Issue 3

Full text available: pdf(1.03 MB) Additional Information: full citation, references, index terms

39 A type based sharing analysis for update avoidance and optimisation

Jörgen Gustavsson

September 1998 **ACM SIGPLAN Notices , Proceedings of the third ACM SIGPLAN international conference on Functional programming**, Volume 34 Issue 1

Full text available: pdf(2.22 MB) Additional Information: full citation, abstract, references, citings, index terms

Sharing of evaluation is crucial for the efficiency of lazy functional languages, but unfortunately the machinery to implement it carries an inherent overhead. In abstract machines this overhead shows up as the cost of performing updates, many of them actually unnecessary, and also in the cost of the associated bookkeeping, that is keeping track of when and where to update. In spineless abstract machines, such as the STG-machine and the TIM, this bookkeeping consists of pushing, checking for and ...

40 Data cashing in IR systems

P. Simpson, R. Alonso

November 1987 **Proceedings of the 10th annual international ACM SIGIR conference on Research and development in information retrieval**

Full text available: pdf(931.70 KB) Additional Information: full citation, abstract, references, citings, index terms

Information retrieval (IR) systems provide individual remote access to centrally managed data. The current proliferation of personal computer systems, as well as advances in storage and communication technology, have created new possibilities for designing information systems which are easily accessible, economical, and responsive to user needs. This paper outlines methods of integrating personal computers (PCs) into large information systems, with emphasis on effective use of the storage a ...

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